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Michael Rajnik



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Microsoft® Application Architecture Guide, 2nd Edition Microsoft Team, 2009 The definitive guide from the Microsoft patterns practices team on designing applications for the Microsoft application platform **NET Application Architecture Guide**, 2009 The guide is intended to serve as a practical and convenient overview of and reference to the general principles of architecture and design on the Microsoft platform and the NET Framework Advanced Information Systems Engineering Workshops Camille Salinesi, Oscar Pastor, 2011-06-17 This book constitutes the thoroughly refereed proceedings of ten international workshops held in London UK in conjunction with the 23rd International Conference on Advanced Information Systems Engineering CAiSE 2011 in June 2011 The 59 revised papers were carefully selected from 139 submissions The ten workshops included Business IT Alignment and Interoperability BUSITAL Conceptualization of Modelling Methods CMM Domain Specific Engineering DsE CAiSE Governance Risk and Compliance GRCIS Integration of IS Engineering Tools INISET System and Software Architectures IWSSA Ontology Driven Information Systems Engineering ODISE Ontology Models Conceptualization and Epistemology in Social Artificial and Natural Systems ONTOSE Semantic Search SSW and Information Systems Security Engineering WISSE *Effective Coding with VHDL* Ricardo Jasinski, 2016-05-27 A guide to applying software design principles and coding practices to VHDL to improve the readability maintainability and quality of VHDL code This book addresses an often neglected aspect of the creation of VHDL designs A VHDL description is also source code and VHDL designers can use the best practices of software development to write high quality code and to organize it in a design This book presents this unique set of skills teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware The concepts introduced here will help readers write code that is easier to understand and more likely to be correct with improved readability maintainability and overall quality After a brief review of VHDL the book presents fundamental design principles for writing code discussing such topics as design quality architecture modularity abstraction and hierarchy Building on these concepts the book then introduces and provides recommendations for each basic element of VHDL code including statements design units types data objects and subprograms The book covers naming data objects and functions commenting the source code and visually presenting the code on the screen All recommendations are supported by detailed rationales Finally the book explores two uses of VHDL synthesis and testbenches It examines the key characteristics of code intended for synthesis distinguishing it from code meant for simulation and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models including combinational sequential and FSM code Examples from the book are also available on a companion website enabling the reader to experiment with the complete source code Proceedings of Sixth International Congress on Information and Communication Technology Xin-She Yang, Simon Sherratt, Nilanjan Dey, Amit Joshi, 2021-09-16 This book gathers selected

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