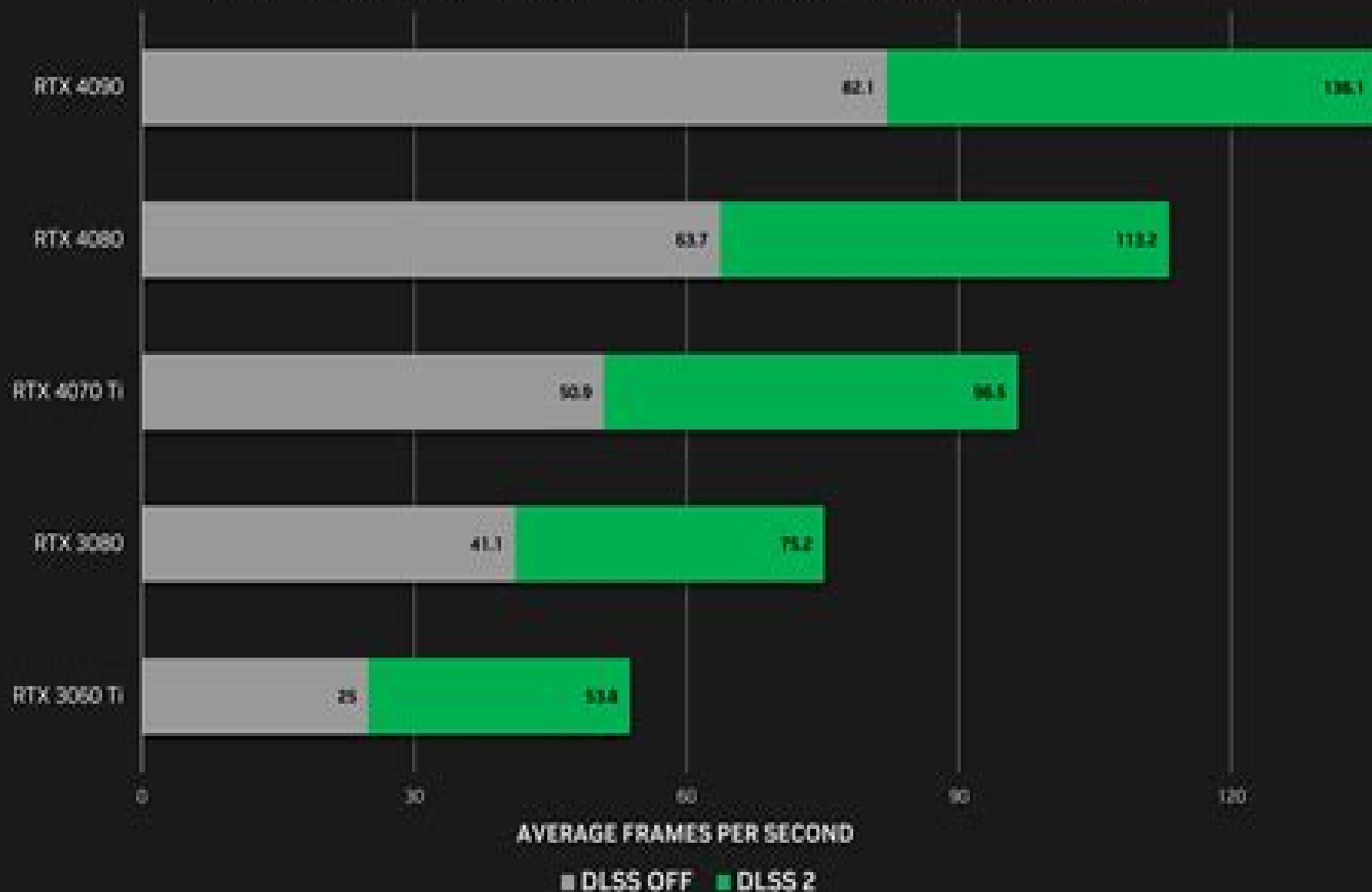


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Lei Shi



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GPU Parallel Program Development Using CUDA Tolga Soyata, 2018-01-19 GPU Parallel Program Development using CUDA teaches GPU programming by showing the differences among different families of GPUs. This approach prepares the reader for the next generation and future generations of GPUs. The book emphasizes concepts that will remain relevant for a long time rather than concepts that are platform specific. At the same time, the book also provides platform dependent explanations that are as valuable as generalized GPU concepts. The book consists of three separate parts: it starts by explaining parallelism using CPU multi threading in Part I. A few simple programs are used to demonstrate the concept of dividing a large task into multiple parallel sub tasks and mapping them to CPU threads. Multiple ways of parallelizing the same task are analyzed and their pros and cons are studied in terms of both core and memory operation. Part II of the book introduces GPU massive parallelism. The same programs are parallelized on multiple Nvidia GPU platforms and the same performance analysis is repeated. Because the core and memory structures of CPUs and GPUs are different, the results differ in interesting ways. The end goal is to make programmers aware of all the good ideas as well as the bad ideas so readers can apply the good ideas and avoid the bad ideas in their own programs. Part III of the book provides pointers for readers who want to expand their horizons. It provides a brief introduction to popular CUDA libraries such as cuBLAS, cuFFT, NPP, and Thrust, the OpenCL programming language, an overview of GPU programming using other programming languages and API libraries such as Python, OpenCV, OpenGL, and Apple's Swift and Metal, and the deep learning library cuDNN. *HWM*, 2005-04. Singapore's leading tech magazine gives its readers the power to decide with its informative articles and in depth reviews.

The History of the GPU - New Developments Jon Peddie, 2023-01-01. This third book in the three part series on the History of the GPU covers the second to sixth eras of the GPU which can be found in anything that has a display or screen. The GPU is now part of supercomputers, PCs, Smartphones, and tablets, wearables, game consoles, and handhelds, TVs, and every type of vehicle including boats and planes. In the early 2000s, the number of GPU suppliers consolidated to three, whereas now the number has expanded to almost 20. In 2022, the GPU market was worth over 250 billion, with over 2.2 billion GPUs being sold just in PCs and more than 10 billion in smartphones. Understanding the power and history of these devices is not only a fascinating tale but one that will aid your understanding of some of the developments in consumer electronics, computers, new automobiles, and your fitness watch.

Optimized Deep Learning on Apple Silicon with PyTorch MPS William Smith, 2025-08-20. Optimized Deep Learning on Apple Silicon with PyTorch MPS is the definitive guide for practitioners and researchers seeking to harness the full power of Apple's cutting edge hardware for machine learning. This comprehensive book begins with an in depth exploration of Apple Silicon's architecture, uncovering how its unified memory design, high performance Neural Engine, and Metal based GPU enable efficient, high throughput AI workloads. Thoughtful comparisons with x86, CUDA, and other AI platforms equip readers with a

nuanced understanding of where Apple Silicon excels and where challenges remain particularly for edge and embedded deployments The text provides an advanced and practical introduction to using PyTorch s Metal Performance Shaders MPS backend covering intricate details of device abstraction operator support memory management and data pipelines Readers will discover best practices for model adaptation quantization pruning and mixed precision training specifically tailored for Apple s unique hardware landscape Step by step optimization techniques ranging from efficient batch loading and asynchronous execution to advanced profiling and performance tuning empower users to maximize model accuracy and throughput while minimizing latency and resource usage Going beyond core concepts the book features real world case studies and hands on guidance for deploying deep learning models at scale both on Apple devices and within hybrid cross platform architectures From distributed training and Kubernetes orchestration to on device inference monitoring and enterprise pipeline integration each chapter anticipates the next generation of challenges and opportunities in AI Alongside a forward looking review of forthcoming Apple hardware and MPS developments this book serves as an essential blueprint for professionals and teams intent on building robust efficient and future proof AI solutions within the expanding Apple ecosystem

Competitive Semiconductor Product Management Sultana Begum,Faiyaz Chowdary,2024-12-23 This book is your guide to navigating the complex landscape of the semiconductor product industry With the emerging benefits of artificial intelligence AI the semiconductor industry is at crossroads of unprecedented demand touching almost every product in the modern world The book takes into consideration the business development opportunities and guides you through technical and business details to help you gain a deep understanding of the semiconductor product management lifecycle From transistors to modern AI accelerators semiconductor products are fundamental contributors to technology and shape our future Continuous research and development are pushing the boundaries of technology disruption and demanding capabilities in semiconductor products in almost all technological spaces This book is a one stop resource for delivering customer centric solutions and it unveils the secrets to crafting the perfect semiconductor product The book takes you through the impact of factors such as The Chips Act 2022 to reveal opportunities and challenges across semiconductor product industries It provides a systematic framework for product managers and technology business leaders to define and implement semiconductor products with competitive advantage using a robust competitive product strategy This book demystifies the complex technical concepts in semiconductor architecture manufacturing technology and performance management via easy to understand real world examples What You Will Learn Develop knowledge of semiconductor hardware architecture and software organization Dig into industry eco system challenges factors contributing to success and failures of semiconductor products Know the secrets to defining competitive semiconductor product strategies and roadmaps Be aware of factors impacting semiconductor product manufacturing and performance management Understand the strategies of the key semiconductor product leaders in the industry Gain a detailed understanding of the design development

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Digit, 2007 **Test Yourself On Build a Large Language Model (From Scratch)**, 2025-07-22 Learn how to create train and tweak large language models LLMs by building one from the ground up Sebastian Raschka's bestselling book Build a Large Language Model From Scratch is the best way to learn how Large Language Models function It uses Python and the PyTorch deep learning library It's a unique way to learn this subject which some believe is the only way to truly learn you build a model yourself Even with the clear explanations diagrams and code in the book learning a complex subject is still hard This Test Yourself guide intends to make it a little easier The structure mirrors the structure of Build a Large Language Model From Scratch focusing on key concepts from each chapter You can test yourself with multiple choice quizzes questions on code and key concepts and questions with longer answers that push you to think critically The answers to all questions are provided Depending on what you know at any point this Test Yourself guide can help you in different ways It will solidify your

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