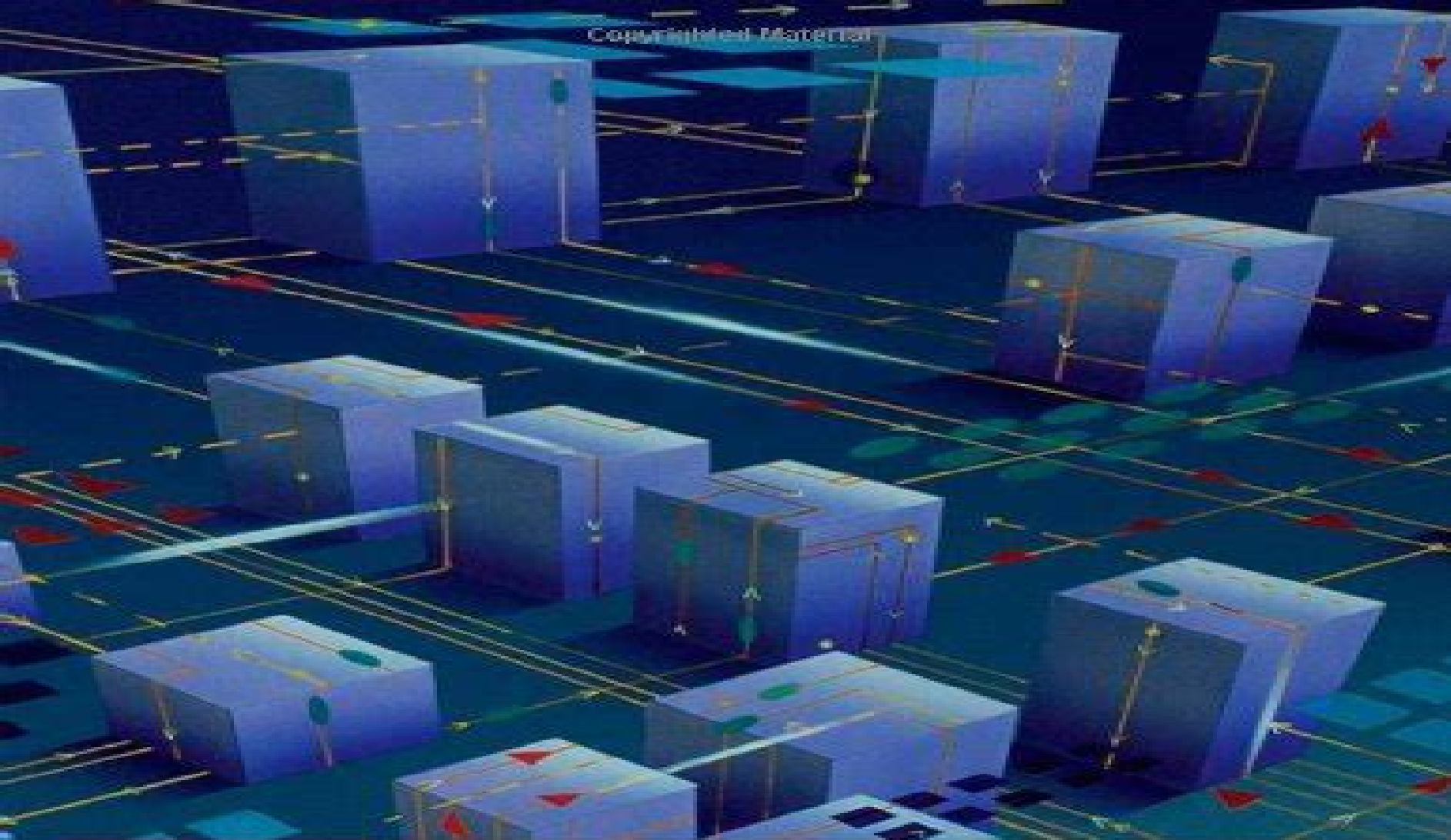




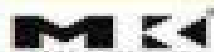
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cutting edge multi core architecture Increasingly demanding modern applications such as those used in telecommunications networking and real time processing of audio video and multimedia streams require multiple processors to achieve computational performance at the rate of a few giga operations per second This necessity for speed and manageable power consumption makes it likely that the next generation of embedded processing systems will include hundreds of cores while being increasingly programmable blending processors and configurable hardware in a power efficient manner Multi Core Embedded Systems presents a variety of perspectives that elucidate the technical challenges associated with such increased integration of homogeneous processors and heterogeneous multiple cores It offers an analysis that industry engineers and professionals will need to understand the physical details of both software and hardware in embedded architectures as well as their limitations and potential for future growth Discusses the available programming models spread across different abstraction levels The book begins with an overview of the evolution of multiprocessor architectures for embedded applications and discusses techniques for autonomous power management of system level parameters It addresses the use of existing open source and free tools originating from several application domains such as traffic modeling graph theory parallel computing and network simulation In addition the authors cover other important topics associated with multi core embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications

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