



Fig. 1.11 The shape of postsynaptic potentials depends on the momentary level of depolarization. (a) A presynaptic spike that arrives at time t_i^j at an inhibitory synapse has hardly any effect on the membrane potential when the neuron is at rest, but a large effect if the membrane potential u is above the resting potential. If the membrane is hyperpolarized below the reversal potential of the inhibitory synapse, the response to the presynaptic input changes sign. (b) A spike at an excitatory synapse evokes a postsynaptic potential with an amplitude that depends only slightly on the momentary voltage u . For large depolarizations the amplitude saturates and becomes smaller. (Schematic figure.)

synapses are often located on the soma or on the shaft of the dendritic tree. Owing to their strategic position, a few inhibitory input spikes can “shunt” the whole input that is gathered by the dendritic tree from hundreds of excitatory synapses. This phenomenon is called “shunting inhibition.”

The reversal potential for excitatory synapses is usually significantly above the resting potential. If the membrane is depolarized $u_0 \gg u_{rest}$ the amplitude of an excitatory postsynaptic potential is reduced, but the effect is not as pronounced as for inhibition. For very high levels of depolarization a saturation of the EPSPs can be observed; see Fig. 1.11b.

1.4.3 Conductance changes after a spike

The shape of the postsynaptic potentials depends not only on the level of depolarization but, more generally, on the internal state of the neuron, e.g., on the timing relative to previous action potentials.

Suppose that an action potential has occurred at time t_i^f and that a presynaptic spike arrives at a time $t_j^f > t_i^f$ at the synapse j . The form of the postsynaptic potential depends now on the time $t_j^f - t_i^f$; see Fig. 1.12. If the presynaptic spike arrives during or shortly after a postsynaptic action potential, it has little effect because some of the ion channels that were involved in firing the action potential are still open. If the input spike arrives much later, it generates a postsynaptic potential of the usual size. We will return to this effect in Chapter 2.

1.4.4 Spatial structure

The form of postsynaptic potentials also depends on the location of the synapse on the dendritic tree. Synapses that are located far away from the soma are expected to evoke a

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Artificial Neural Networks and Machine Learning - ICANN 2024 Michael Wand, Kristína Malinovská, Jürgen Schmidhuber, Igor V. Tetko, 2024-09-16 The ten volume set LNCS 15016 15025 constitutes the refereed proceedings of the 33rd International Conference on Artificial Neural Networks and Machine Learning ICANN 2024 held in Lugano Switzerland during September 17 20 2024 The 294 full papers and 16 short papers included in these proceedings were carefully reviewed and selected from 764 submissions The papers cover the following topics Part I theory of neural networks and machine learning novel methods in machine learning novel

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Neuromorphic Computing Principles and Organization Abderazek Ben Abdallah, Khanh N. Dang, 2025-04-23 The second edition of Neuromorphic Computing Principles and Organization delves deeply into neuromorphic computing focusing on designing fault tolerant scalable hardware for spiking neural networks Each chapter includes exercises to enhance understanding All existing chapters have been meticulously revised and a new chapter on advanced neuromorphic prosthesis design serves as a comprehensive case study The book starts with an overview of neuromorphic systems and fundamental artificial neural network concepts It explores artificial neurons neuron models storage technologies inter neuron communication learning mechanisms and design approaches Detailed discussions cover challenges in constructing spiking neural networks and emerging memory technologies A dedicated chapter addresses circuits and architectures including Network on Chip NoC fabric Address Event Representation AER memory access methods and photonic interconnects Reliability issues recovery methods for multicore systems and reconfigurable designs supporting multiple applications are examined The book also describes the hardware software design of a three dimensional neuromorphic processor focusing on high integration density minimal spike delay and scalable design The book concludes with a comprehensive review of neuromorphic systems providing a detailed analysis of the field and an overarching understanding of the key concepts discussed throughout the text

Advances in Computer Graphics Nadia Magnenat-Thalmann, Jinman Kim, Bin Sheng, Zhigang Deng, Daniel Thalmann, Ping Li, 2025-03-01 The three volume set LNCS 15338 15339 and 15340 constitutes the refereed proceedings from the 41st Computer Graphics International Conference CGI 2024 held during July 1 5 2024 in Geneva Switzerland The 84 full papers presented in these proceedings were carefully reviewed and selected from 211 submissions The papers are organized in the following topical sections Part I Colors painting

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