

Application of Microsystems Technology in the Fabrication of Thermoelectric Micro-Converters

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1. Introduction

The use of thin-film deposition techniques with microsystems technologies renewed the interest in the thermoelectricity in the last years. Integration of efficient solid-state thermoelectric (TE) microdevices with microelectronics is desirable for local cooling and, since they can be used to stabilise the temperature of devices, decrease noise levels and increase operation speed. Their use in thermoelectric microgeneration (energy harvesting) can also supply energy to low power consumption electronic devices. In this chapter, the fabrication of thermoelectric microconverters is compared, both on materials from thin-film composites to superlattice structures, and on its fabrication techniques.

Various materials can be used for this type of converters. However, for room temperature application, Bi/Sb/Te compounds are still the most efficient thermoelectric materials. Recently, efforts were made to apply quantum confinement to thermoelectric materials, and the results are thin-film superlattice structures and nanowires and even more recently, bulk nanocomposites. Some of these materials proved the ability to double efficiency of current thermoelectric devices. Several deposition techniques can be used for the fabrication of Bi/Sb/Te thin-films: co-sputtering, electrochemical deposition, metal-organic chemical vapor deposition or flash evaporation are some examples compared here.

The patterning process must use photolithography techniques to create the small dimensions of these devices. Despite these techniques are commonly used in microelectronic devices, mainly with silicon based substrates, its application in other thermoelectric alloys is still under development.

The patterning of thermoelectric structures for the fabrication of thermoelectric microconverters can be done using common microsystems technologies. Techniques used in MEMS fabrication, namely wet-etching, lift-off (with SU-8 photoresist), Reactive Ion Etching (RIE) and Lithography-Electroplating-Molding (LEIM) are here compared for the fabrication of thermoelectric microsystems.

2. Theory behind thermoelectric devices

There are two groups of applications for thermoelectric materials based on Seebeck and Peltier effects respectively. In the Seebeck effect, a temperature difference between the junctions of two different materials produces an electric voltage (figure 1), and an electric

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An Introduction

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Multimedia Technologies: Concepts, Methodologies, Tools, and Applications Syed, Mahbubur Rahman,2008-06-30 This book offers an in depth explanation of multimedia technologies within their many specific application areas as well as presenting developing trends for the future Provided by publisher

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in microelectronics and or more generally in electrical engineering microsystems and material sciences The 2004 edition of European Workshop on Microelectronics Education EWME is particularly focused on the interface between microelectronics and bio medical sciences **Design of Systems on a Chip: Design and Test** Ricardo Reis, Marcelo Soares

Lubaszewski, Jochen A.G. Jess, 2007-05-06 This book is the second of two volumes addressing the design challenges associated with new generations of semiconductor technology The various chapters are compiled from tutorials presented at workshops in recent years by prominent authors from all over the world Technology productivity and quality are the main aspects under consideration to establish the major requirements for the design and test of upcoming systems on a chip

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as well as hinting at emerging future technologies *Circuits and Systems in the Information Age* Yih-Fang Huang, 1997

1995 International Symposium on Microelectronics International Symposium on Microelectronics (28, 1995, Los Angeles, Calif.), 1995

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