

Date: 01 April 2014

Federal Communications Commission Authorization and Evaluation Devision 7435 Oakland Mills Road Columbia, MD 21046

Attn: OET Dept.

Ref: FCC Class II Permissive change for FCC ID: VOB-P1640

Original Grant Date: 09/09/2013 Applicant: NVIDIA Corporation

Dear Examiner,

This is to request a Class II permissive change for FCC ID: VOB-P1640, originally granted on 09/09/2013

The major change filed under this application is:

- The non-transmitter components/functions is being modified to improve the holistic performance relevant to un-intentional radiator part
- 2. Bluetooth Low Energy Mode is being enabled via software/firmware update

Auxiliary Note:

- The equivalent type of antenna with similar in-band, out of band characteristics of lower gain is replaced. Printed Antenna with 3.45dBi.
- Spot-checked of the re-evaluation on EMC spurious, and RFx SAR was done to verify the performance of the compliance
- The result of the spot-check as detailed, and contained in respective test report reveals there is no occurrence of the degradation

Sincerely yours,

Charles Jackson / Compliance Engineering Manager

NVIDIA Corporation

Cover Letter Nvidia Gpu Today

Roman Wyrzykowski, Jack Dongarra, Konrad Karczewski, Jerzy Wasniewski

Cover Letter Nvidia Gpu Today:

Research and Technical Writing for Science and Engineering Meikang Qiu, Han Qiu, Yi Zeng, 2022-02-27 Engineering and science research can be difficult for beginners because scientific research is fraught with constraints and disciplines Research and Technical Writing for Science and Engineering breakdowns the entire process of conducting engineering and scientific research This book covers those fascinating guidelines and topics on conducting research as well as how to better interact with your advisor Key Features advice on conducting a literature review conducting experiments and writing a good paper summarizing your findings provides a tutorial on how to increase the impact of research and how to manage research resources By reflecting on the cases discussed in this book readers will be able to identify specific situations or dilemmas in their own lives as the authors provide comprehensive suggestions based on their own experiences **Design and Development** Rob Farber, 2011-10-08 As the computer industry retools to leverage massively parallel graphics processing units GPUs this book is designed to meet the needs of working software developers who need to understand GPU programming with CUDA and increase efficiency in their projects CUDA Application Design and Development starts with an introduction to parallel computing concepts for readers with no previous parallel experience and focuses on issues of immediate importance to working software developers achieving high performance maintaining competitiveness analyzing CUDA benefits versus costs and determining application lifespan The book then details the thought behind CUDA and teaches how to create analyze and debug CUDA applications Throughout the focus is on software engineering issues how to use CUDA in the context of existing application code with existing compilers languages software tools and industry standard API libraries Using an approach refined in a series of well received articles at Dr Dobb's Journal author Rob Farber takes the reader step by step from fundamentals to implementation moving from language theory to practical coding Includes multiple examples building from simple to more complex applications in four key areas machine learning visualization vision recognition and mobile computing Addresses the foundational issues for CUDA development multi threaded programming and the different memory hierarchy Includes teaching chapters designed to give a full understanding of CUDA tools techniques and structure Presents CUDA techniques in the context of the hardware they are implemented on as well as other styles of programming that will help readers bridge into the new material Parallel Processing and Applied Mathematics, Part I Roman Wyrzykowski, Jack Dongarra, Konrad Karczewski, Jerzy Wasniewski, 2010-07-07 This book constitutes the proceedings of the 8th International Conference on Parallel Processing and Applied Mathematics PPAM 2009 held in Wroclaw Poland in September 2009 **Contemporary High Performance Computing** Jeffrey S. Vetter, 2017-11-23 Contemporary High Performance Computing From Petascale toward Exascale focuses on the ecosystems surrounding the world's leading centers for high performance computing HPC It covers many of the important factors involved in each ecosystem computer architectures software applications facilities and sponsors The first part of the book examines

significant trends in HPC systems including computer architectures applications performance and software It discusses the growth from terascale to petascale computing and the influence of the TOP500 and Green500 lists The second part of the book provides a comprehensive overview of 18 HPC ecosystems from around the world Each chapter in this section describes programmatic motivation for HPC and their important applications a flagship HPC system overview covering computer architecture system software programming systems storage visualization and analytics support and an overview of their data center facility The last part of the book addresses the role of clouds and grids in HPC including chapters on the Magellan FutureGrid and LLGrid projects With contributions from top researchers directly involved in designing deploying and using these supercomputing systems this book captures a global picture of the state of the art in HPC **Game Face** ,2003

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