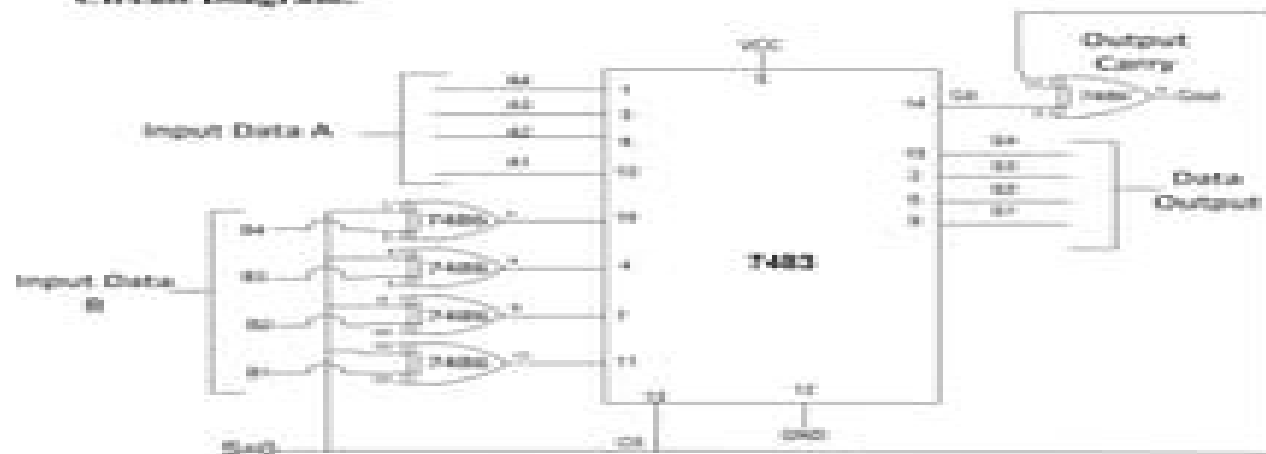




A. IC 7483 as a Parallel Adder

Circuit Diagram:



Truth Table:-

4-BIT Parallel Adder Using 7483 where S=0

Input Data A				Input Data B				Addition				
A4	A3	A2	A1	B4	B3	B2	B1	Carry	S4	S3	S2	S1
1	0	0	0	0	0	1	0	0	1	0	1	0
1	0	0	0	1	0	0	0	1	0	0	0	0
0	0	1	0	1	0	0	0	0	1	0	1	0
0	0	0	1	0	1	1	1	0	1	0	0	0
1	0	1	0	1	0	1	1	1	0	1	0	1
0	1	1	0	0	0	1	1	0	1	0	0	1
1	1	1	0	1	1	1	1	1	1	1	0	1
1	0	1	0	1	1	0	1	1	0	1	1	1

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