# MICROPROCESSOR AND INTERFACING (EE-329-F)

# LAB MANUAL

# V SEMESTER





Department of Electronics & Communication Engineering Dronacharya College of Engineering Khentawas, Gurgaon – 123506

# **Microprocessor Interfacing Techniques Lab Manual**

Theodore F. Bogart

# **Microprocessor Interfacing Techniques Lab Manual:**

Practical Microprocessor Interfacing S. A. Money,1987-11-02 This book looks at effective techniques for interfacing between the microprocessor and external peripheral equipment Offering a balanced treatment of both hardware and software applications it begins by discussing the structure and timing of signals on microprocessor bus systems and the memory devices used to store and retrieve data Goes on to provide practical coverage of the parallel interface its underlying principles and techniques of implementation including how to handle both dedicated and memory mapped input output systems programmable I O port devices and some specially designed parallel interface chips Provides examples of typical applications of I O ports including the connection of switches and keyboards the drive circuits of lamps and relays and driving stepper type monitors Also discussed are two standard parallel interface schemes which are commonly used with microcomputer systems Other topics cover the basics of synchronous and asynchronous serial transmission the advantage of external hardware in implementing counting and timing functions interfacing digital and analogue converters to the CPU bus system and effective ways of checking the input lines

\*\*Bowker's Complete Sourcebook of Personal Computing, 1985\*\*
R.R. Bowker Company,1984 Provides Listings of Hardware Software Peripherals Currently Available as Well as Books

\*\*Magazines Clubs User Groups Virtually All Other Microcomputer related Services Includes Background Information Glossary

Computer Books and Serials in Print, 1985 **Subject Guide to Books in Print** ,1997 **Introductory Circuit** Analysis Robert L. Boylestad, 1990-02 Robotics, CAD/CAM Market Place, 1985, 1985 16/32 Bit Microprocessors Wunnava V. Subbarao, 1991 An integrated practical introduction to 16 bit and 32 bit microprocessors using the Motorola 68000 family as examples for electronics engineering computer science and technology students Digital Experiments David Buchla, 1990 Scientific and Technical Aerospace Reports ,1995 **Digital Experiments Emphasizing Troubleshooting** Jerry V. Cox,1990 **Practical Electronics Handbook** Ian Sinclair, 2013-10-22 This is the best value handbook on electronics you can buy With new chapters and sections covering topics such as sensing components connectors soldering and unsoldering this fourth edition contains all of the everyday information that anyone working in electronics will need It provides a practical and comprehensive collection of circuits rules of thumb and design data for professional engineers students and enthusiasts and therefore enough background to allow the understanding and development of a Digital Fundamentals Thomas L. Floyd, 1990 range of basic circuits **Area Array Interconnection Handbook** Karl J. Puttlitz, Paul A. Totta, 2012-12-06 Microelectronic packaging has been recognized as an important enabler for the solid state revolution in electronics which we have witnessed in the last third of the twentieth century Packaging has provided the necessary external wiring and interconnection capability for transistors and integrated circuits while they have gone through their own spectacular revolution from discrete device to gigascale integration At IBM we are proud to have created the initial simple concept of flip chip with solder bump connections at a time when a better way was needed to boost the reliability and

improve the manufacturability of semiconductors The basic design which was chosen for SLT Solid Logic Technology in the 1960s was easily extended to integrated circuits in the 70s and VLSI in the 80s and 90s Three I O bumps have grown to 3000 with even more anticipated for the future The package families have evolved from thick film SLT to thin film metallized ceramic to co fired multi layer ceramic A later family or ceramics with matching expansivity to sili con and copper internal wiring was developed as a predecessor of the chip interconnection revolution in copper multilevel submicron wiring Powerful server packages have been de veloped in which the combined chip and package copper wiring exceeds a kilometer All of this was achieved with the constant objective of minimizing circuit delays through short efficient interconnects **Catalog**Southwestern Indian Polytechnic Institute,1994 **Encyclopedia of Microfluidics and Nanofluidics** Dongqing

Li,2008-08-06 Covering all aspects of transport phenomena on the nano and micro scale this encyclopedia features over 750 entries in three alphabetically arranged volumes including the most up to date research insights and applied techniques across all areas Coverage includes electrical double layers optofluidics DNC lab on a chip nanosensors and more

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