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Microelectronic Test Structures for CMOS Technology

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John G. Webster, Halit Eren



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Microelectronic Test Structures for CMOS Technology Manjul Bhushan, Mark B. Ketchen, 2011-08-26

Microelectronic Test Structures for CMOS Technology and Products addresses the basic concepts of the design of test structures for incorporation within test vehicles scribe lines and CMOS products The role of test structures in the development and monitoring of CMOS technologies and products has become ever more important with the increased cost and complexity of development and manufacturing In this timely volume IBM scientists Manjul Bhushan and Mark Ketchen emphasize high speed characterization techniques for digital CMOS circuit applications and bridging between circuit performance and characteristics of MOSFETs and other circuit elements Detailed examples are presented throughout many of which are equally applicable to other microelectronic technologies as well The authors overarching goal is to provide students and technology practitioners alike a practical guide to the disciplined design and use of test structures that give unambiguous information on the parametrics and performance of digital CMOS technology

CMOS Test and Evaluation Manjul Bhushan, Mark B. Ketchen, 2014-12-03 CMOS Test and Evaluation A Physical Perspective is a single source for an integrated view of test and data analysis methodology for CMOS products covering circuit sensitivities to MOSFET characteristics impact of silicon technology process variability applications of embedded test structures and sensors product yield and reliability over the lifetime of the product This book also covers statistical data analysis and visualization techniques test equipment and CMOS product specifications and examines product behavior over its full voltage temperature and frequency range

Tradeoffs and Optimization in Analog CMOS Design David Binkley, 2008-09-15 Analog CMOS integrated circuits are in widespread use for communications entertainment multimedia biomedical and many other applications that interface with the physical world Although analog CMOS design is greatly complicated by the design choices of drain current channel width and channel length present for every MOS device in a circuit these design choices afford significant opportunities for optimizing circuit performance This book addresses tradeoffs and optimization of device and circuit performance for selections of the drain current inversion coefficient and channel length where channel width is implicitly considered The inversion coefficient is used as a technology independent measure of MOS inversion that permits design freely in weak moderate and strong inversion This book details the significant performance tradeoffs available in analog CMOS design and guides the designer towards optimum design by describing An interpretation of MOS modeling for the analog designer motivated by the EKV MOS model using tabulated hand expressions and figures that give performance and tradeoffs for the design choices of drain current inversion coefficient and channel length performance includes effective gate source bias and drain source saturation voltages transconductance efficiency transconductance distortion normalized drain source conductance capacitances gain and bandwidth measures thermal and flicker noise mismatch and gate and drain leakage current Measured data that validates the inclusion of important small geometry effects like velocity saturation

vertical field mobility reduction drain induced barrier lowering and inversion level increases in gate referred flicker noise voltage In depth treatment of moderate inversion which offers low bias compliance voltages high transconductance efficiency and good immunity to velocity saturation effects for circuits designed in modern low voltage processes Fabricated design examples that include operational transconductance amplifiers optimized for various tradeoffs in DC and AC performance and micropower low noise preamplifiers optimized for minimum thermal and flicker noise A design spreadsheet available at the book web site that facilitates rapid optimum design of MOS devices and circuits Tradeoffs and Optimization in Analog CMOS Design is the first book dedicated to this important topic It will help practicing analog circuit designers and advanced students of electrical engineering build design intuition rapidly optimize circuit performance during initial design and minimize trial and error circuit simulations

Reliability Wearout Mechanisms in Advanced CMOS Technologies Alvin W. Strong, Ernest Y. Wu, Rolf-Peter Vollertsen, Jordi Sune, Giuseppe La Rosa, Timothy D. Sullivan, Stewart E. Rauch, III, 2009-10-13 This invaluable resource tells the complete story of failure mechanisms from basic concepts to the tools necessary to conduct reliability tests and analyze the results Both a text and a reference work for this important area of semiconductor technology it assumes no reliability education or experience It also offers the first reference book with all relevant physics equations and step by step procedures for CMOS technology reliability in one place Practical appendices provide basic experimental procedures that include experiment design performing stressing in the laboratory data analysis reliability projections and interpreting projections

The ESD Handbook Steven H. Voldman, 2021-03-02 A practical and comprehensive reference that explores Electrostatic Discharge ESD in semiconductor components and electronic systems The ESD Handbook offers a comprehensive reference that explores topics relevant to ESD design in semiconductor components and explores ESD in various systems Electrostatic discharge is a common problem in the semiconductor environment and this reference fills a gap in the literature by discussing ESD protection Written by a noted expert on the topic the text offers a topic by topic reference that includes illustrative figures discussions and drawings The handbook covers a wide range of topics including ESD in manufacturing garments wrist straps and shoes ESD Testing ESD device physics ESD semiconductor process effects ESD failure mechanisms ESD circuits in different technologies CMOS Bipolar etc ESD circuit types Pin Power Pin to Pin etc and much more In addition the text includes a glossary index tables illustrations and a variety of case studies Contains a well organized reference that provides a quick review on a range of ESD topics Fills the gap in the current literature by providing information from purely scientific and physical aspects to practical applications Offers information in clear and accessible terms Written by the accomplished author of the popular ESD book series Written for technicians operators engineers circuit designers and failure analysis engineers The ESD Handbook contains an accessible reference to ESD design and ESD systems

VLSI-SoC: Research Trends in VLSI and Systems on Chip Giovanni De Micheli, Salvador Mir, Ricardo Reis, 2010-08-23 This book contains extended and revised versions of the best papers presented during the fourteenth IFIP

TC 10 WG 10 5 International Conference on Very Large Scale Integration This conference provides a forum to exchange ideas and show industrial and academic research results in microelectronics design The current trend toward increasing chip integration and technology process advancements brings about stimulating new challenges both at the physical and system design levels

Matching Properties of Deep Sub-Micron MOS Transistors Jeroen A. Croon, Willy M Sansen, Herman E. Maes, 2006-06-20 Matching Properties of Deep Sub Micron MOS Transistors examines this interesting phenomenon Microscopic fluctuations cause stochastic parameter fluctuations that affect the accuracy of the MOSFET For analog circuits this determines the trade off between speed power accuracy and yield Furthermore due to the down scaling of device dimensions transistor mismatch has an increasing impact on digital circuits The matching properties of MOSFETs are studied at several levels of abstraction A simple and physics based model is presented that accurately describes the mismatch in the drain current The model is illustrated by dimensioning the unit current cell of a current steering D A converter The most commonly used methods to extract the matching properties of a technology are bench marked with respect to model accuracy measurement accuracy and speed and physical contents of the extracted parameters The physical origins of microscopic fluctuations and how they affect MOSFET operation are investigated This leads to a refinement of the generally applied 1 area law In addition the analysis of simple transistor models highlights the physical mechanisms that dominate the fluctuations in the drain current and transconductance The impact of process parameters on the matching properties is discussed The impact of gate line edge roughness is investigated which is considered to be one of the roadblocks to the further down scaling of the MOS transistor Matching Properties of Deep Sub Micron MOS Transistors is aimed at device physicists characterization engineers technology designers circuit designers or anybody else interested in the stochastic properties of the MOSFET

Functional Nanostructures and Sensors for CBRN Defence and Environmental Safety and Security Anatolie Sidorenko, Horst Hahn, 2020-03-17 Over the last decade techniques for materials preparation and processing at nanometer scale have advanced rapidly leading to the introduction of novel principles for a new generation of sensors and detectors At the same time the chemical industry transport and agriculture produce huge amounts of dangerous waste gases and liquids leading to soil air and water contamination One more modern threat international terrorism demands that scientists make efforts to apply new principles and technologies to protect society against chemical biological radiological and nuclear CBRN attacks and to develop novel effective technologies for the remediation of large contaminated areas Accordingly the main goal of this book is to bring together experts theorists experimentalists engineers and technologists for an extensive discussion covering novel principles for functional nanostructures and detector fabrication and implementation the development of novel technologies for the deactivation of CBRN agents their experimental realization and their application in novel monitoring and control systems and technological processes for soil and water remediation with a view to environmental protection and defence against CBRN based terrorism In keeping with the book s

main goal the following topics are highlighted and discussed Sensors and detectors detection of chemicals principles of artificial nose and chemical micro lab on a chip design surface and underground water quality monitoring systems molecular electronics superconducting electronic devices quantum detectors and Qubits Environmental protection and CBRN detection of infrared microwave X ray and terahertz radiation Principles for novel IR UV and Terahertz wave devices for the detection of low contrast objects Novel technological processes for CBRN destruction and deactivation All these topics are strongly interrelated both with regard to fundamental aspects and to fabrication and implementation technologies in addition they are highly promising for application in novel functional devices computer logics sensing and detection of low concentration chemicals weak and extremely weak magnetic and microwave fields infrared and ultraviolet radiation Given its scope the book will be a useful and interesting guide for a broad readership of engineers scientists PhD students and experts in the area of defence against environmental terrorism

ESD Steven H. Voldman, 2006-02-03 The scaling of semiconductor devices from sub micron to nanometer dimensions is driving the need for understanding the design of electrostatic discharge ESD circuits and the response of these integrated circuits IC to ESD phenomena ESD Circuits and Devices provides a clear insight into the layout and design of circuitry for protection against electrical overstress EOS and ESD With an emphasis on examples this text explains ESD buffering ballasting current distribution design segmentation feedback coupling and decoupling ESD design methods outlines the fundamental analytical models and experimental results for the ESD design of MOSFETs and diode semiconductor device elements with a focus on CMOS silicon on insulator SOI and Silicon Germanium SiGe technology focuses on the ESD design optimization integration and synthesis of these elements and concepts into ESD networks as well as applying within the off chip driver networks and on chip receivers and highlights state of the art ESD input circuits as well as ESD power clamps networks Continuing the author's series of books on ESD this book will be an invaluable reference for the professional semiconductor chip and system ESD engineer Semiconductor device and process development quality reliability and failure analysis engineers will also find it an essential tool In addition both senior undergraduate and graduate students in microelectronics and IC design will find its numerous examples useful

SOC Design Methodologies Michel Robert, Bruno Rouzeyre, Christian Pigué, Marie-Lise Flottes, 2013-03-15 The 11th IFIP International Conference on Very Large Scale Integration in Montpellier France December 3-5 2001 was a great success The main focus was about IP Cores Circuits and System Designs 55 years ago in late 1947 the transistor was invented and everybody knows that it was by William Shockley John Bardeen and Walter H Brattain Bell Telephone Laboratories which received the Nobel Prize in Physics in 1956 Probably everybody thinks that it was recognized immediately as a major invention

ESD Design and Analysis Handbook James E. Vinson, Joseph C. Bernier, Gregg D. Croft, Juin-Jei Liou, 2012-12-06 ESD Design and Analysis Handbook presents an overview of ESD as it affects electronic circuits and provides a concise introduction for students engineers circuit designers and failure analysts This handbook is written in

simple terms and is filled with practical advice and examples to illustrate the concepts presented While this treatment is not exhaustive it presents many of the most important areas of the ESD problem and suggests methods for improving them The key topics covered include the physics of the event failure analysis protection characterization and simulation techniques The book is intended as both an introductory text on ESD and a useful reference tool to draw on as the reader gains experience The authors have tried to balance the level of detail in the ESD Design and Analysis Handbook against the wealth of literature published on ESD every year To that end each chapter has a topical list of references to facilitate further in depth study

Measurement, Instrumentation, and Sensors Handbook John G. Webster, Halit Eren, 2017-12-19 The Second Edition of the bestselling Measurement Instrumentation and Sensors Handbook brings together all aspects of the design and implementation of measurement instrumentation and sensors Reflecting the current state of the art it describes the use of instruments and techniques for performing practical measurements in engineering physics chemistry and the life sciences and discusses processing systems automatic data acquisition reduction and analysis operation characteristics accuracy errors calibrations and the incorporation of standards for control purposes Organized according to measurement problem the Electromagnetic Optical Radiation Chemical and Biomedical Measurement volume of the Second Edition Contains contributions from field experts new chapters and updates to all 98 existing chapters Covers sensors and sensor technology time and frequency signal processing displays and recorders and optical medical biomedical health environmental electrical electromagnetic and chemical variables A concise and useful reference for engineers scientists academic faculty students designers managers and industry professionals involved in instrumentation and measurement research and development Measurement Instrumentation and Sensors Handbook Second Edition Electromagnetic Optical Radiation Chemical and Biomedical Measurement provides readers with a greater understanding of advanced applications

Publications of the National Institute of Standards and Technology ... Catalog National Institute of Standards and Technology (U.S.), National Institute of Standards and Technology (U.S.). Information Resources and Services Division, 1994

Publications of the National Institute of Standards and Technology 1988 Catalog Rebecca J. Pardee, National Institute of Standards and Technology (U.S.), 1989

Measurement, Instrumentation, and Sensors Handbook, Second Edition John G. Webster, Halit Eren, 2014-02-03 The Second Edition of the bestselling Measurement Instrumentation and Sensors Handbook brings together all aspects of the design and implementation of measurement instrumentation and sensors Reflecting the current state of the art it describes the use of instruments and techniques for performing practical measurements in engineering physics chemistry and the life sciences and discusses processing systems automatic data acquisition reduction and analysis operation characteristics accuracy errors calibrations and the incorporation of standards for control purposes Organized according to measurement problem the Electromagnetic Optical Radiation Chemical and Biomedical Measurement volume of the Second Edition Contains contributions from field experts new chapters and updates to all 98 existing chapters

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Gm-C Filter Synthesis for Modern RF Systems Vančo Litovski, 2021-11-30 This book discusses synthesis of Gm C filter for modern radio frequency systems Analogue filters are an inevitable part of the chain of signal processing in modern radio and telecommunication systems Among the technologies implemented for these purposes are the so called Gm C filters which are built of operational transconductance amplifiers and capacitors This technology allows for integration of the filter into a CMOS system on chip so making it very attractive for application in low power battery operated devices The objective of this book is to achieve three goals 1 to fully describe the circuit synthesis procedures for parallel cascade and a realization based on LC prototypes 2 to make a thorough evaluation of the advantages and disadvantages of these methods and to recommend the preferable one and 3 to create an extensive table of element values of cascaded Gm C filters realizing the best known low pass filter functions The book will influence the design community to embrace this technology even for non communication applications

Low-Temperature Technologies and Applications Md Salim Newaz Kazi, 2022-03-30 This book on low temperature technology is a notable collection of different aspects of the technology and its application in varieties of research and practical engineering fields It contains sterilization and preservation techniques and their engineering and scientific characteristics Ultra low temperature refrigeration the refrigerants applications and economic aspects are highlighted in this issue The readers will find the low temperature and vacuum systems for industrial applications This book has given attention to global energy resources conservation of energy and alternative sources of energy for the application of low temperature technologies

Semiconductor Measurement Technology United States. National Bureau of Standards, 1983

Measurement and Modeling of Silicon Heterostructure Devices John D. Cressler, 2018-10-03 When you see a nicely presented set of data the natural response is How did they do that what tricks did they use and how can I do that for myself Alas usually you must simply keep wondering since such tricks of the trade are usually held close to the vest and rarely divulged Shamefully ignored in the technical literature measurement and modeling of high speed semiconductor devices is a fine art Robust measuring and modeling at the levels of performance found in modern SiGe devices requires extreme dexterity in the laboratory to obtain reliable data and then a valid model to fit that data Drawn from the comprehensive and well reviewed Silicon Heterostructure Handbook this volume focuses on measurement and modeling of high speed silicon heterostructure devices The chapter authors provide experience based tricks of the trade and the subtle nuances of measuring and modeling advanced devices making this an important reference

for the semiconductor industry It includes easy to reference appendices covering topics such as the properties of silicon and germanium the generalized Moll Ross relations the integral charge control model and sample SiGe HBT compact model parameters *The Fourth Terminal* Sylvain Clerc,Thierry Di Gilio,Andreia Cathelin,2020-04-25 This book discusses the advantages and challenges of Body Biasing for integrated circuits and systems together with the deployment of the design infrastructure needed to generate this Body Bias voltage These new design solutions enable state of the art energy efficiency and system flexibility for the latest applications such as Internet of Things and 5G communications

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